

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Gormley et al.

GROUP: 2813

SERIAL NO: 10/786,426

EXAMINER: Tuan H. Nguyen

FILED: 02/25/04

FOR: SEMICONDUCTOR WAFER COMPRISING MICRO-MACHINED
COMPONENTS AND A METHOD FOR FABRICATING THE
SEMICONDUCTOR WAFER

Mail Stop Issue Fee
Assistant Commissioner of Patents
P.O. Box 1450, Alexandria, VA 22313-1450

Attn: Official Draftsman

TRANSMITTAL OF FORMAL DRAWINGS

Applicant submits herewith new drawing(s) for this application. Attached please find nine (9) sheets of formal drawings for this application.

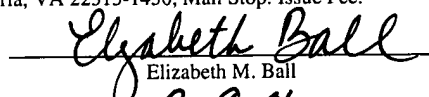
The three month period of response set in the Notice of Allowability (PTOL 37) expires on March 30, 2006, and this submission is on or before this expiry date.

Respectfully submitted,



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I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to the Commissioner of Patents and Trademarks, P.O. Box 1450, Alexandria, VA 22313-1450, Mail Stop: Issue Fee.


Elizabeth M. Ball
Date: 3-9-06